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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/519,551	03/06/2000	Jozeph W. Triepels	PHN 17, 326	8962

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EXAMINER

ABDULSELAM, ABBAS L

ART UNIT PAPER NUMBER

2674

DATE MAILED: 06/05/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.  
09/519,551

Applicant(s)  
Triepels

Examiner  
Abbas Abdulsalam

Group Art Unit  
2674



☒ Responsive to communication(s) filed on Mar 20, 2002

☐ This action is **FINAL**.

☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

A shortened statutory period for response to this action is set to expire 3 month(s), or thirty days, whichever is longer, from the mailing date of this communication. Failure to respond within the period for response will cause the application to become abandoned. (35 U.S.C. § 133). Extensions of time may be obtained under the provisions of 37 CFR 1.136(a).

## Disposition of Claims

☒ Claim(s) 1-10 is/are pending in the application.

Of the above, claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

☐ Claim(s) \_\_\_\_\_ is/are allowed.

☒ Claim(s) 1-10 is/are rejected.

☐ Claim(s) \_\_\_\_\_ is/are objected to.

☐ Claims \_\_\_\_\_ are subject to restriction or election requirement.

## Application Papers

☐ See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948.

☐ The drawing(s) filed on \_\_\_\_\_ is/are objected to by the Examiner.

☐ The proposed drawing correction, filed on \_\_\_\_\_ is ☐ approved ☐ disapproved.

☐ The specification is objected to by the Examiner.

☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. § 119

☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).

☐ All ☐ Some\* ☐ None of the CERTIFIED copies of the priority documents have been

☐ received.

☐ received in Application No. (Series Code/Serial Number) \_\_\_\_\_

☐ received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\*Certified copies not received: \_\_\_\_\_

☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

## Attachment(s)

☒ Notice of References Cited, PTO-892

☐ Information Disclosure Statement(s), PTO-1449, Paper No(s). \_\_\_\_\_

☐ Interview Summary, PTO-413

☐ Notice of Draftsperson's Patent Drawing Review, PTO-948

☐ Notice of Informal Patent Application, PTO-152

--- SEE OFFICE ACTION ON THE FOLLOWING PAGES ---

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## **DETAILED ACTION**

### **Claim Rejections 35 U.S.C. 103**

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1-10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ueda et al. (USPN 5838400) in view of Gofuku et al. (USPN 5973761).

Regarding claim 1, Ueda teaches a display device with first and second substrates connected eclectically with the ground pad of common metallic shield. Ueda teaches A first substrate which is made of multilayered flexible pad pattern via holes for electric connection; and a surface conductor layer. Ueda also teaches a second substrate which also consists a surface conductor layer as well; and has electrical connection through holes. See column 5, lines 9-24. Furthermore, Ueda teaches an interface substrate that can be connected in a reliable way even if the number of pixels increase. See column 20, lines 1-4. Ueda does not teach laminar substrate with electrically conducting patterns on opposite sides. Gofuku on the other hand teaches a liquid crystal device whose formation include one substrate with laminar structure and an electrical property control layer. See col.2, lines 35-47.

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Therefore, it would have been obvious to one skilled in the art at the time the invention was made to modify Ueda's LCD device to include Gofuku's laminar substrate configured with an electrical property control layer. One would have been motivated in view of the suggestion in Gofuku that a laminar substrate with electrical property layer is equivalent to the desired laminar substrate with electrical conducting patterns. The use of laminar substrate helps form a liquid crystal device as taught by Gofuku.

Regarding claim 2, Ueda teaches shield casins metal frame. Ueda also teaches electronic parts related to shield casins mounted in a flexible substrates the portion of which is composed of conductor. See column 4, lines 41-44.

Regarding claim 3, Ueda teaches a conductor layer L3 which is gold plated. See column 14, lines 57-59.

Regarding claims 4-5, and 8, Ueda teaches that a multi layer flexible substrate has its portion made of conductor layers which is electrically connected with insulating substrate through conductive film. See column 4, lines 24-30.

Regarding claim 6, Ueda teaches a display device with flexible substrate. See column 4, lines 5-14.

Regarding claim 7, Ueda teaches the wiring substrate which is made of multilayered flexible substrate producing increased wiring density and increases pattern of conductor. See column 6, lines 34-45.

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Regarding claim 9, Ueda teaches applying a voltage to common transparent pixel electrode relative to configuration of substrate (sub 1) and (substrate sub 2). See column 16, lines 49-56.

Regarding claim 10, Ueda teaches liquid crystal display device with thin film. See column 1, lines 29-43

### **Conclusion**

2. The prior art made of record and not relied upon is considered to applicant's disclosure.

The following arts are cited for further reference.

U.S. Pat No. 5,812,227 to Toshida et al.

U.S. Pat No. 5,972,527 to Kaijou et al.

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3.. Any inquiry concerning this communication or earlier communication from the examiner should be directed to **Abbas Abdulsalam** whose telephone number is **(703) 305-8591**. The examiner can normally be reached on Monday through Friday (9:00-5:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **Richard Hjerpe**, can be reached at **(703) 305-4709**.

**Any response to this action should be mailed to:**

Commissioner of patents and Trademarks

Washington, D.C. 20231

**or faxed to:**

**(703) 872-9314**

Hand delivered responses should be brought to crustal park II, Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology center 2600 customer Service office whose telephone number is (703) 306-0377.



**RICHARD HJERPE**  
**SUPERVISORY PATENT EXAMINER**  
**TECHNOLOGY CENTER 2600**

Abbas Abdulsalam

Examiner

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